	Application No.	Applicant(s)
Notice of Allowability	09/671,949	TUGENBERG ET AL.
	Examiner	Art Unit	
	Abdulhakim Nobahar	2132	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--
All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to _03/24/2006_.

2. ☒ The allowed claim(s) is/are _1-21_.

3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a) ☐ All   b) ☐ Some*   c) ☐ None of the:

        1. ☐ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____ .

    (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____ .

    Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)
1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____ .

DETAILED ACTION

1.   This communication is in response to appellants' response received on March 3,

2006.

2.   Appellants' arguments with respect to the rejections of claims 1-21 under 35 USC

§ 103 mailed June 22, 2005 have been fully considered and are persuasive. Therefore,

the rejections have been withdrawn.

Allowable Subject Matter

1.   Claims 1-21 are allowed.

2.   The following is an examiner's statement of reasons for allowance:

The primary reasons for the allowance of the independent claims 1, 12 and 19

are the inclusion of the following limitations that are not found in the prior art and they

are uniquely distinct features. The closest prior art are Janssen et al. (5,954,817) and

Cassagnol et al. (6,385,727 B1).

Janssen discloses an apparatus for securing electronic information that includes

a non-volatile memory and a microcontroller. The memory is externally coupled to the

microcontroller. The microcontroller comprises a one-time programmable security flag

that is programmed when the electronic information is stored in the memory. Once

programmed, the security flag prevents reprogramming of the memory. Additionally, Janssen discloses that the microcontroller comprises an encryption algorithm that generates electronic authentication information during programming of the electronic information.

Cassagnol discloses a secure processing environment that includes non-volatile memory, a logic circuit for controlling access to the data contained in the non-volatile memory directly connected to a key isolation circuit, and a number of logic gates. Cassagnol further discloses an external memory for storing encrypted data. Cassagnol also discloses that the encrypted data is imported from the external storage medium to the internal memory and decrypts the data to be used by the processor and re-encrypted for storage in the external memory.

However, these two arts, singularly or in combination, fail to anticipate or render the following limitation:

"Claim 1: wherein sensitive data is encrypted by the encryption logic circuitry directly using the laser-scribed encryption key and stored as encrypted data in a data storage medium, and

wherein the encrypted data is decrypted by the encryption logic circuitry directly using the laser-scribed encryption key and transferred to the memory for use by the host processor."

"Claim 12: wherein sensitive data is encrypted within the secure memory directly using the laser-scribed encryption key and stored as encrypted data in the non-secure memory, and

wherein the encrypted data is decrypted within the secure memory directly using the laser-scribed encryption key and stored within the secure memory for use by the host processor."

"Claim 19: encrypting sensitive data within the secure memory directly using the laser-scribed encryption key;

storing the encrypted sensitive data in the non-secure memory;

decrypting the encrypted sensitive data within the secure memory directly using the laser-scribed encryption key; and storing the decrypted sensitive data within the secure memory for use by the host processor."

3.   The dependent claims 2-11, 13-18 and 20-21 are allowed because they were originally found to include a unique feature not found in the closest abovementioned art.

4.   Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5.   Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Abdulhakim Nobahar whose telephone number is 571-

272-3808. The examiner can normally be reached on M-T 8-6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Gilberto Barron can be reached on 571-272-3799. The fax phone number

for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

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Abdulhakim Nobahar
Examiner
Art Unit 2132
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June 1, 2006

GILBERTO BARRON JR.
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100